

Prodigy Advanced Square Protocol and Command Structure

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Last Updated August 5th, 2012

This is the documentation to understand the protocol and command structure of the MRC Prodigy Advanced Square DCC controller. After purchasing the MRC computer interface we used a serial snoop to identify the protocol and command structure of the Cab Bus of the MRC Prodigy System. We also have a homemade DCC bus monitor made from a Parallax Propeller Microcontroller. Both buses are read into a Visual Basic program that allows us to debug both buses simultaneously. Additionally, we have added a "Virtual Cab" in the VB program. So far this "Virtual Cab" can control engines with speed and direction along with their corresponding functions. It can also control stationary decoders.

The purpose of this document was to list the command structure on both the Cab bus and the DCC bus. We have not yet decoded the decoder programming side of the Cab controllers but focused on the operation side of the decoders.

You need to purchase the MRC wired or wireless interface and then setup a serial monitor to see the commands as shown in the Cab Bus Command Tab. It is worth noting that all Cabs talk on the Cab Bus with no hardware flow control. They respond to a "Polled" communication protocol established by the Prodigy base station. From MRC suggested practices and forum advice you should establish each controller ID by using every other or every 3rd available address. This helps prevent one Cab from "stepping" on the communication of the previous Cab if it is slow in its polled response. Additionally, the first Cab address (0) comes from the Command Station and includes data for the System Fast Clock. This is very useful if you want to use a PC controller and keep it in time sync with the Handheld clocks.

Serial Parameters

Parameter Group	Parameter	PC Setting	Hardwired Bus
Baud Control	Baud Rate	38,400	19200
	High Byte	\$4B	
	Low Byte	\$00	
Line Control	Parity, stop, data bits	1,0,8	
	Parity (0=None, 1=Odd, 2=Even)	Odd	Odd
	Data Bits = 8	8	9
	Stop Bits (0=1stop, 2=2stop)	1 stop	1 stop
Handshake Control		None	
		None	
Time Out	Transmit -1		
	Read = 0		
Buffer			

Baud Rate Note: We wrote the program with the settings listed in the PC column. Later we measured with a Digital Line Analyzer and it appears to be the Hardwired Bus column. We don't know what happens in the Wireless Bus interface sold by MRC and it could be it converts uses the 8bit 38400 rate on the PC side while the Wireless side may convert over to the Hardwired Bus speed. This faster PC side may have also been needed to minimize the USB delay which as shown in the Digital Line Analyzer could cause problems in the MRC Polled buss system.

Stationary Accessory Address

Entered Address	Packet Address	Addr Byte 0	Data Byte 0
Notes: Format:		b7 set, b6 clear	b7,6,5,3 set A8 is inverted D is 1=on 0=off
		1,0,A7,A6,A5,A4,A3,A2	1,1,1,!A8,1,A1,A0,D
0	Not used		
1	4	10000001	11111000
2	5	10000001	11111010
3	6	10000001	11111100
4	7	10000001	11111110
5	8	10000010	11111000
6	9	10000010	11111010
7	10	10000010	11111100
8	11	10000010	11111110
9	12	10000011	11111000
10	13	10000011	11111010
11	14	10000011	11111100
12	15	10000011	11111110
13	16	10000100	11111000
14	17	10000100	11111010
15	18	10000100	11111100
16	19	10000100	11111110
17	20	10000101	11111000
18	21	10000101	11111010
19	22	10000101	11111100
20	23	10000101	11111110
21	24	10000110	11111000
22	25	10000110	11111010
23	26	10000110	11111100
24	27	10000110	11111110
25	28	10000111	11111000
26	29	10000111	11111010
27	30	10000111	11111100
28	31	10000111	11111110
29	32	10001000	11111000
30	33	10001000	11111010
31	34	10001000	11111100
32	35	10001000	11111110
33	36	10001001	11111000
34	37	10001001	11111010
35	38	10001001	11111100
36	39	10001001	11111110
37	40	10001010	11111000

38	41	10001010	11111010
39	42	10001010	11111100
40	43	10001010	11111110
41	44	10001011	11111000
42	45	10001011	11111010
43	46	10001011	11111100
44	47	10001011	11111110
45	48	10001100	11111000
46	49	10001100	11111010
47	50	10001100	11111100
48	51	10001100	11111110
49	52	10001101	11111000
50	53	10001101	11111010
51	54	10001101	11111100
52	55	10001101	11111110
53	56	10001110	11111000
54	57	10001110	11111010
55	58	10001110	11111100
56	59	10001110	11111110
57	60	10001111	11111000
58	61	10001111	11111010
59	62	10001111	11111100
60	63	10001111	11111110
61	64	10010000	11111000
62	65	10010000	11111010
63	66	10010000	11111100
64	67	10010000	11111110
65	68	10010001	11111000
66	69	10010001	11111010
67	70	10010001	11111100
68	71	10010001	11111110
69	72	10010010	11111000
70	73	10010010	11111010
71	74	10010010	11111100
72	75	10010010	11111110
73	76	10010011	11111000
74	77	10010011	11111010
75	78	10010011	11111100
76	79	10010011	11111110
77	80	10010100	11111000
78	81	10010100	11111010
79	82	10010100	11111100
80	83	10010100	11111110
81	84	10010101	11111000
82	85	10010101	11111010
83	86	10010101	11111100
84	87	10010101	11111110

85	88	10010110	11111000
86	89	10010110	11111010
87	90	10010110	11111100
88	91	10010110	11111110
89	92	10010111	11111000
90	93	10010111	11111010
91	94	10010111	11111100
92	95	10010111	11111110
93	96	10011000	11111000
94	97	10011000	11111010
95	98	10011000	11111100
96	99	10011000	11111110
97	100	10011001	11111000
98	101	10011001	11111010
99	102	10011001	11111100
100	103	10011001	11111110
101	104	10011010	11111000
102	105	10011010	11111010
103	106	10011010	11111100
104	107	10011010	11111110
105	108	10011011	11111000
106	109	10011011	11111010
107	110	10011011	11111100
108	111	10011011	11111110
109	112	10011100	11111000
110	113	10011100	11111010
111	114	10011100	11111100
112	115	10011100	11111110
113	116	10011101	11111000
114	117	10011101	11111010
115	118	10011101	11111100
116	119	10011101	11111110
117	120	10011110	11111000
118	121	10011110	11111010
119	122	10011110	11111100
120	123	10011110	11111110
121	124	10011111	11111000
122	125	10011111	11111010
123	126	10011111	11111100
124	127	10011111	11111110
125	128	10100000	11111000
126	129	10100000	11111010
127	130	10100000	11111100
128	131	10100000	11111110
129	132	10100001	11111000
130	133	10100001	11111010
131	134	10100001	11111100

132	135	10100001	11111110
133	136	10100010	11111000
134	137	10100010	11111010
135	138	10100010	11111100
136	139	10100010	11111110
137	140	10100011	11111000
138	141	10100011	11111010
139	142	10100011	11111100
140	143	10100011	11111110
141	144	10100100	11111000
142	145	10100100	11111010
143	146	10100100	11111100
144	147	10100100	11111110
145	148	10100101	11111000
146	149	10100101	11111010
147	150	10100101	11111100
148	151	10100101	11111110
149	152	10100110	11111000
150	153	10100110	11111010
151	154	10100110	11111100
152	155	10100110	11111110
153	156	10100111	11111000
154	157	10100111	11111010
155	158	10100111	11111100
156	159	10100111	11111110
157	160	10101000	11111000
158	161	10101000	11111010
159	162	10101000	11111100
160	163	10101000	11111110
161	164	10101001	11111000
162	165	10101001	11111010
163	166	10101001	11111100
164	167	10101001	11111110
165	168	10101010	11111000
166	169	10101010	11111010
167	170	10101010	11111100
168	171	10101010	11111110
169	172	10101011	11111000
170	173	10101011	11111010
171	174	10101011	11111100
172	175	10101011	11111110
173	176	10101100	11111000
174	177	10101100	11111010
175	178	10101100	11111100
176	179	10101100	11111110
177	180	10101101	11111000
178	181	10101101	11111010

179	182	10101101	11111100
180	183	10101101	11111110
181	184	10101110	11111000
182	185	10101110	11111010
183	186	10101110	11111100
184	187	10101110	11111110
185	188	10101111	11111000
186	189	10101111	11111010
187	190	10101111	11111100
188	191	10101111	11111110
189	192	10110000	11111000
190	193	10110000	11111010
191	194	10110000	11111100
192	195	10110000	11111110
193	196	10110001	11111000
194	197	10110001	11111010
195	198	10110001	11111100
196	199	10110001	11111110
197	200	10110010	11111000
198	201	10110010	11111010
199	202	10110010	11111100
200	203	10110010	11111110
201	204	10110011	11111000
202	205	10110011	11111010
203	206	10110011	11111100
204	207	10110011	11111110
205	208	10110100	11111000
206	209	10110100	11111010
207	210	10110100	11111100
208	211	10110100	11111110
209	212	10110101	11111000
210	213	10110101	11111010
211	214	10110101	11111100
212	215	10110101	11111110
213	216	10110110	11111000
214	217	10110110	11111010
215	218	10110110	11111100
216	219	10110110	11111110
217	220	10110111	11111000
218	221	10110111	11111010
219	222	10110111	11111100
220	223	10110111	11111110
221	224	10111000	11111000
222	225	10111000	11111010
223	226	10111000	11111100
224	227	10111000	11111110
225	228	10111001	11111000

226	229	10111001	11111010
227	230	10111001	11111100
228	231	10111001	11111110
229	232	10111010	11111000
230	233	10111010	11111010
231	234	10111010	11111100
232	235	10111010	11111110
233	236	10111011	11111000
234	237	10111011	11111010
235	238	10111011	11111100
236	239	10111011	11111110
237	240	10111100	11111000
238	241	10111100	11111010
239	242	10111100	11111100
240	243	10111100	11111110
241	244	10111101	11111000
242	245	10111101	11111010
243	246	10111101	11111100
244	247	10111101	11111110
245	248	10111110	11111000
246	249	10111110	11111010
247	250	10111110	11111100
248	251	10111110	11111110
249	252	10111111	11111000
250	253	10111111	11111010
251	254	10111111	11111100
252	255	10111111	11111110
253	256	10000000	11101000
254	257	10000000	11101010
255	258	10000000	11101100

Speed log

	Command		address		Value	checksum
1410-F0-on	34	34	c5	82	90	53
1410-F0-off	34	34	c5	82	80	53
1410-F1-on	34	34	c5	82	81	c6
1410-F1-off	34	34	c5	82	80	c7
1286-F1-on	34	34	c5	6	81	42
1286-F1-off	34	34	c5	6	80	43
1286-F2-on	34	34	c5	6	82	41
1286-F3-on	34	34	c5	6	84	47
1286-F4-on	34	34	c5	6	88	4b
1286-F5-on	44	44	c5	6	b1	72
1286-F6-on	44	44	c5	6	b2	71
1286-F7-on	44	44	c5	6	b4	77
1286-F8-on	44	44	c5	6	b8	7b
1286-F9-on	54	54	c5	6	A1	62
1286-F10-on	54	54	c5	6	A2	61
1286-F11-on	54	54	c5	6	A4	67
1286-F12-on	54	54	c5	6	A8	6B
1286-F13-on	74	74	c5	6	D1	12
1286-F14-on	74	74	c5	6	D2	11
1286-F15-on	74	74	c5	6	D4	17
1286-F16-on	74	74	c5	6	D8	1B
1286-F17-on	84	84	c5	6	C1	1B
1286-F18-on	84	84	c5	6	C2	12
1286-F19-on	84	84	c5	6	C4	12
1286-F20-on	84	84	c5	6	C8	12
1286-F21-on	A4	A4	c5	6	1	C2
1286-F22-on	A4	A4	c5	6	2	C1
1286-F23-on	A4	A4	c5	6	4	c7
1286-F24-on	A4	A4	c5	6	8	CB
1286-F25-on	A4	A4	c5	6	10	D3
1286-F26-on	A4	A4	c5	6	20	E3
1286-F27-on	A4	A4	c5	6	40	83
1286-F28-on	A4	A4	c5	6	80	43
1286 fwd NO	25	25	c5	6	40	0
1286 FWD 10	25	25	c5	6	76	
1286 FWD 5	25	25	c5	6	64	0
1286 FWD 0	25	25	c5	6	60	0
1286 REV 0	25	25	c5	6	50	
1286 REV 5	25	25	c5	6	44	
1286 REV 10	25	25	c5	6	56	
					128=DIR	128 SPEED
1286 SPEED	25	25	c5	6	FF	2
1287 SPEED	25	25	c5	6	C1	2
1288 SPEED	25	25	c5	6	C0	2
1289 SPEED	25	25	c5	6	BF	2
1290 SPEED	25	25	c5	6	BE	2
1291 SPEED	25	25	c5	6	A1	2
1292 SPEED	25	25	c5	6	91	2

Speed log

1293	SPEED	25	25	c5	6	89	2
1294	SPEED	25	25	c5	6	85	2
1295	SPEED	25	25	c5	6	83	2
1296	SPEED	25	25	c5	6	82	2
1297	SPEED	25	25	c5	6	81	2
1298	SPEED	25	25	c5	6	80	2
1299	SPEED	25	25	c5	6	7F	2
1300	SPEED	25	25	c5	6	3F	2
1301	SPEED	25	25	c5	6	1	2
1302	SPEED	25	25	c5	6	0	2

hex	74	34	c5	82	A8	c6
bin	01110100	00110100	11000101	10000010	10101000	11000110
dec	116	52	197	130	168	198

1410

1410 Dec to Hex 582
 Dec to Bin
 Hex to Bin
 Hex to Dec

Speed log

83

A7

A3

126

64

63

62

61

32

16

7F

79

28

Speed log

8	69		
4	69		
2	79		
1			
0			
STOP			
-128			
-64			
-1			
0			
40	10	11	12
01000000	00010000	00010001	00010010
64	16	17	18
-64			

Serial Control log

0	IRP_MJ_CREATE	DOWN	TRUE
1	IRP_MJ_CREATE	UP	FALSE
2	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_QUEUE_SIZE: Set queue size)	DOWN	TRUE
3	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_QUEUE_SIZE: Set queue size)	UP	FALSE
4	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_BAUD_RATE: Retrieve Baud Rate)	DOWN	FALSE
5	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_BAUD_RATE: Retrieve Baud Rate)	UP	TRUE
6	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_LINE_CONTROL: Retrieve line control)	DOWN	FALSE
7	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_LINE_CONTROL: Retrieve line control)	UP	TRUE
8	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_CHARS: Retrieve special characters)	DOWN	FALSE
9	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_CHARS: Retrieve special characters)	UP	TRUE
10	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_HANDFLOW: Retrieve handshake information)	DOWN	FALSE
11	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_HANDFLOW: Retrieve handshake information)	UP	TRUE
12	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_BAUD_RATE: Set baud rate)	DOWN	TRUE
13	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_BAUD_RATE: Set baud rate)	UP	FALSE
14	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_CLR_RTS: Clear RTS)	DOWN	FALSE
15	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_CLR_RTS: Clear RTS)	UP	FALSE
16	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_CLR_DTR: Clear DTR)	DOWN	FALSE
17	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_CLR_DTR: Clear DTR)	UP	FALSE
18	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_LINE_CONTROL: Set line control)	DOWN	TRUE
19	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_LINE_CONTROL: Set line control)	UP	FALSE
20	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_CHARS: Set special characters)	DOWN	TRUE
21	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_CHARS: Set special characters)	UP	FALSE
22	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_HANDFLOW: Set handshake information)	DOWN	TRUE
23	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_HANDFLOW: Set handshake information)	UP	FALSE
24	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_TIMEOUTS: Set timeouts)	DOWN	TRUE
25	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_TIMEOUTS: Set timeouts)	UP	FALSE
26	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_PURGE: Purge requests)	DOWN	TRUE
27	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_PURGE: Purge requests)	UP	FALSE
28	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_WAIT_MASK: Set current event mask)	DOWN	TRUE
29	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
30	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
31	IRP_MJ_WRITE	DOWN	TRUE
32	IRP_MJ_WRITE	UP	TRUE
33	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_WAIT_MASK: Set current event mask)	UP	FALSE
34	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
35	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
36	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
37	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
38	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
39	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
40	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
41	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
42	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
43	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
44	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
45	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
46	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
47	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
48	IRP_MJ_READ	DOWN	FALSE
49	IRP_MJ_READ	UP	TRUE
50	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
51	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
52	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
53	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
54	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
55	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
56	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
57	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
58	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
59	IRP_MJ_READ	DOWN	FALSE
60	IRP_MJ_READ	UP	TRUE
61	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
62	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
63	IRP_MJ_WRITE	DOWN	TRUE
64	IRP_MJ_WRITE	UP	TRUE
65	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE

Serial Control log

66	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
67	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
68	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
69	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
70	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
71	IRP_MJ_READ	DOWN	FALSE
72	IRP_MJ_READ	UP	TRUE
73	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
74	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
75	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
76	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
77	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
78	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
79	IRP_MJ_READ	DOWN	FALSE
80	IRP_MJ_READ	UP	TRUE
81	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
82	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
83	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
84	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
85	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
86	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
87	IRP_MJ_READ	DOWN	FALSE
88	IRP_MJ_READ	UP	TRUE
89	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
90	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
91	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
92	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
93	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
94	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
95	IRP_MJ_READ	DOWN	FALSE
96	IRP_MJ_READ	UP	TRUE
97	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
98	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
99	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
100	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
101	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
102	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
103	IRP_MJ_READ	DOWN	FALSE
104	IRP_MJ_READ	UP	TRUE
105	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
106	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
107	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
108	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
109	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
110	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
111	IRP_MJ_READ	DOWN	FALSE
112	IRP_MJ_READ	UP	TRUE
113	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
114	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
115	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
116	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
117	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
118	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
119	IRP_MJ_READ	DOWN	FALSE
120	IRP_MJ_READ	UP	TRUE
121	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
122	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
123	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
124	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
125	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
126	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
127	IRP_MJ_READ	DOWN	FALSE
128	IRP_MJ_READ	UP	TRUE
129	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
130	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
131	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE

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132	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
133	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
134	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
135	IRP_MJ_READ	DOWN	FALSE
136	IRP_MJ_READ	UP	TRUE
137	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
138	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
139	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
140	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
141	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
142	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
143	IRP_MJ_READ	DOWN	FALSE
144	IRP_MJ_READ	UP	TRUE
145	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
146	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
147	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
148	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
149	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
150	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
151	IRP_MJ_READ	DOWN	FALSE
152	IRP_MJ_READ	UP	TRUE
153	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
154	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
155	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
156	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
157	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
158	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
159	IRP_MJ_READ	DOWN	FALSE
160	IRP_MJ_READ	UP	TRUE
161	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
162	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
163	IRP_MJ_WRITE	DOWN	TRUE
164	IRP_MJ_WRITE	UP	TRUE
165	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
166	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
167	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
168	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
169	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
170	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
171	IRP_MJ_READ	DOWN	FALSE
172	IRP_MJ_READ	UP	TRUE
173	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
174	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
175	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
176	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
177	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
178	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
179	IRP_MJ_READ	DOWN	FALSE

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180	IRP_MJ_READ	UP	TRUE
264	IRP_MJ_WRITE	UP	TRUE
265	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
266	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
267	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
268	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
269	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
270	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
271	IRP_MJ_READ	DOWN	FALSE
272	IRP_MJ_READ	UP	TRUE
273	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
274	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
275	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
276	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
277	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
278	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
279	IRP_MJ_READ	DOWN	FALSE
280	IRP_MJ_READ	UP	TRUE
281	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
282	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
283	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
284	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
285	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
286	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
287	IRP_MJ_READ	DOWN	FALSE
288	IRP_MJ_READ	UP	TRUE
289	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE

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356	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
357	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
358	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
359	IRP_MJ_READ	DOWN	FALSE
360	IRP_MJ_READ	UP	TRUE
361	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
362	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
363	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
364	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
365	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
366	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
367	IRP_MJ_READ	DOWN	FALSE
368	IRP_MJ_READ	UP	TRUE
369	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
370	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
371	IRP_MJ_WRITE	DOWN	TRUE
372	IRP_MJ_WRITE	UP	TRUE
373	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
374	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
375	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
376	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
377	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
378	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
379	IRP_MJ_READ	DOWN	FALSE
380	IRP_MJ_READ	UP	TRUE
381	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
382	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
383	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
384	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
385	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
386	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
387	IRP_MJ_READ	DOWN	FALSE
388	IRP_MJ_READ	UP	TRUE
389	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
390	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
391	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
392	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
393	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
394	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
395	IRP_MJ_READ	DOWN	FALSE
396	IRP_MJ_READ	UP	TRUE
397	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
398	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
399	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
400	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
401	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
402	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
403	IRP_MJ_READ	DOWN	FALSE
404	IRP_MJ_READ	UP	TRUE
405	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
406	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
407	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
408	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
409	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
410	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
411	IRP_MJ_READ	DOWN	FALSE
412	IRP_MJ_READ	UP	TRUE
413	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
414	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
415	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
416	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
417	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
418	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
419	IRP_MJ_READ	DOWN	FALSE
420	IRP_MJ_READ	UP	TRUE
421	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE

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884	IRP_MJ_READ	UP	TRUE
885	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
886	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
887	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
888	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
889	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
890	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
891	IRP_MJ_READ	DOWN	FALSE
892	IRP_MJ_READ	UP	TRUE
893	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
894	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
895	IRP_MJ_WRITE	DOWN	TRUE
896	IRP_MJ_WRITE	UP	TRUE
897	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
898	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
899	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
900	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
901	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
902	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
903	IRP_MJ_READ	DOWN	FALSE
904	IRP_MJ_READ	UP	TRUE
905	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
906	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
907	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
908	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
909	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
910	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
911	IRP_MJ_READ	DOWN	FALSE
912	IRP_MJ_READ	UP	TRUE
913	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
914	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
915	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
916	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
917	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
918	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
919	IRP_MJ_READ	DOWN	FALSE
920	IRP_MJ_READ	UP	TRUE
921	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
922	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
923	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
924	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
925	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
926	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
927	IRP_MJ_READ	DOWN	FALSE
928	IRP_MJ_READ	UP	TRUE
929	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
930	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
931	IRP_MJ_WRITE	DOWN	TRUE
932	IRP_MJ_WRITE	UP	TRUE
933	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
934	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
935	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
936	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	DOWN	FALSE
937	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	DOWN	FALSE
938	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_GET_COMMSTATUS: Retrieve COM status)	UP	TRUE
939	IRP_MJ_READ	DOWN	FALSE
940	IRP_MJ_READ	UP	TRUE
941	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_WAIT_MASK: Set current event mask)	DOWN	TRUE
942	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_WAIT_ON_MASK: Wait for event)	UP	TRUE
943	IRP_MJ_DEVICE_CONTROL (IOCTL_SERIAL_SET_WAIT_MASK: Set current event mask)	UP	FALSE
944	IRP_MJ_CLOSE	DOWN	FALSE
945	IRP_MJ_CLOSE	UP	FALSE

Serial Control log

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0x0          4D 00 52 00 43 00 44 00 43 00 43 00 5F 00 31 00 5F 00 31 00 2E 00 65 D.C.C._1._1...e.x.e...
0x0
0x0      8096      00 20 00 00 00 20 00 00      . . . .
0x0
0x0
0x0      38400     00 96 00 00      ...
0x0
0x0          00 01 08      ...
0x0
0x0      none     00 00 00 00 00 00      .....
0x0
0x0          00 00 00 00 00 00 00 00 10 00 00 00 04 00 00      .....
0x0      38400     00 96 00 00      ...
0x0
0x0
0x0
0x0
0x0
0x0          00 01 08      ...
0x0
0x0          00 00 00 00 00 00      .....
0x0
0x0      none     00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00      .....
0x0
0x0          FF FF FF FF 00 00 00 00 00 00 00 00 00 00 00 88 13 00 00      yyy.....
0x0
0x0      Purge All  0F 00 00 00      ...
0x0
0x0      Error and Char 81 00 00 00      ...
0x0
0x0          00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 44 89      .....D
0x0          EE EF      ĩ
0x0          EE EF      ĩ
0x0
0x0
0x0          80 00 00 00      ...
0x0
0x0          04 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 AF 89      .....-
0x0
0x0          01 00 00 00      ...
0x0
0x0          00 00 00 00 00 00 00 00 08 00 00 00 00 00 00 00 AF 89      .....-
0x0
0x0          01 00 00 00      ...
0x0
0x0          00 00 00 00 00 00 00 00 0E 00 00 00 00 00 00 00 44 89      .....D
0x0
0x0          00 00 00 00 00 00 00 00 0E 00 00 00 00 00 00 00 44 89      .....D
0x0
0x0          55 A5 00 01 05 00 0B 00 00 01 05 00 0B 00      U¥.....
0x0
0x0
0x0          00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 44 89      .....D
0x0          01 00 00 00      ...
0x0
0x0          00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 AF 89      .....-
0x0
0x0          00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 00 00      .....
0x0
0x0          01 01 01 00 01 00      .....
0x0
0x0
0x0          00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00      .....
0x0          73 00 73 00 88 00 FF 00 77 00      s.s..y.w.
0x0          73 00 73 00 88 00 FF 00 77 00      s.s..y.w.
0x0          01 00 00 00      ....

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Serial Control log

0x0		
0x0	00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 00 00 00 00
0x0		
0x0		
0x0	00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 00 00 00 00
0x0		
0x0	1B 01 EF 00 02 00	..I..
0x0	01 00 00 00
0x0		
0x0	00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 00 00 00 00
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0x0	00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 00 00 00 00
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Serial Control log

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0x0	01 01 01 00 01 00 02
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0x0	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0x0	25 00 25 00 C5 00 82 00 00 00 02 00 45 00	%.%.Ä.....E.
0x0	25 00 25 00 C5 00 82 00 00 00 02 00 45 00	%.%.Ä.....E.
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0x0	01 00 00 00
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Serial Control log

0x0	22 00 03 01 03 00 03 00 00, 181,IRP_MJ_DE VICE_CONTRO L (IOCTL_SERIAL _WAIT_ON_MA SK: Wait for event),UP,TRUE, 0x0,01 00 00 00
0x0	25 00 25 00 00 00 20 00 00 00 02 00 22 00, 182,IRP_MJ_DE VICE_CONTRO L (IOCTL_SERIAL _GET_COMMST ATUS: Retrieve COM status),DOWN,F ALSE,0x0,, 183,IRP_MJ_DE VICE_CONTRO L (IOCTL_SERIAL _GET_COMMST ATUS: Retrieve COM status),UP,TRUE ,0x0,00 00 00 00 00 00 00 00 05 00 00 00 00 00 00 00 00 00 00 00 ,....., 184,IRP_MJ_DE VICE_CONTRO L (IOCTL_SERIAL _WAIT_ON_MA SK: Wait for %.%... ..".
0x0	01 00 00 00
0x0	00 00 00 00 00 00 00 00 09 00 00 00 00 00 00 00 00 00 00 00
0x0	00 00 00 00 00 00 00 00 09 00 00 00 00 00 00 00 00 00 00 00
0x0	55 00 55 00 22 00 22 00 02	U.U.". ".
0x0	01 00 00 00
0x0	00 00 00 00 00 00 00 00 09 00 00 00 00 00 00 00 00 00 00 00
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0x0	03 00 00 01 05 00 0B 00
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Serial Control log

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0x0	73 00 73 00 81 00 FF 00 7E 00	s.s..ÿ.~.
0x0	73 00 73 00 81 00 FF 00 7E 00	s.s..ÿ.~.
0x0	01 00 00 00
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0x0	73 00 73 00 81 00 FF 00 7E 00	s.s..ÿ.~.
0x0	73 00 73 00 81 00 FF 00 7E 00	s.s..ÿ.~.
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0x0	01 01 01 00 01 00
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0x0	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
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0x0	C3 00 C3 00 10 00 00 00 10 00	Ā.Ā.....
0x0	01 00 00 00
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0x0	01 01 01 00 01 00
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0x0	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0x0	C3 00 C3 00 08 00 80 00 88 00	Ā.Ā.....
0x0	C3 00 C3 00 08 00 80 00 88 00	Ā.Ā.....
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Serial Control log

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0x0	01 01 01 00 01 00
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0x0	00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
0x0	C3 00 C3 00 1F 00 00 00 1F 00	Ä.Ä.....
0x0	C3 00 C3 00 1F 00 00 00 1F 00	Ä.Ä.....
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Serial Control log

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0x0	00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 00 00 00 00
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Serial Control log

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0x0	00 00 00 00 00 00 00 00 06 00 00 00 00 00 00 00 00 00 00 00
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Serial Control log

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0x0	00 00 00 00 00 00 00 00 02 00 00 00 00 00 00 00 00 00 00 00
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Serial Control log

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Serial Control log

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Serial Control log

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Serial Control log

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